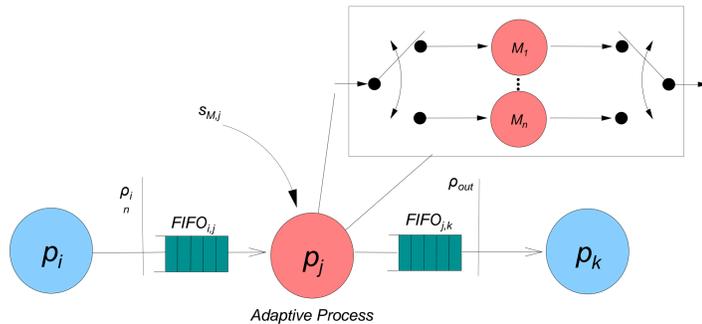
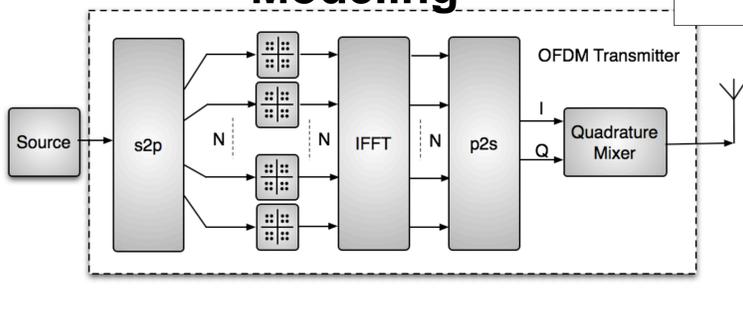




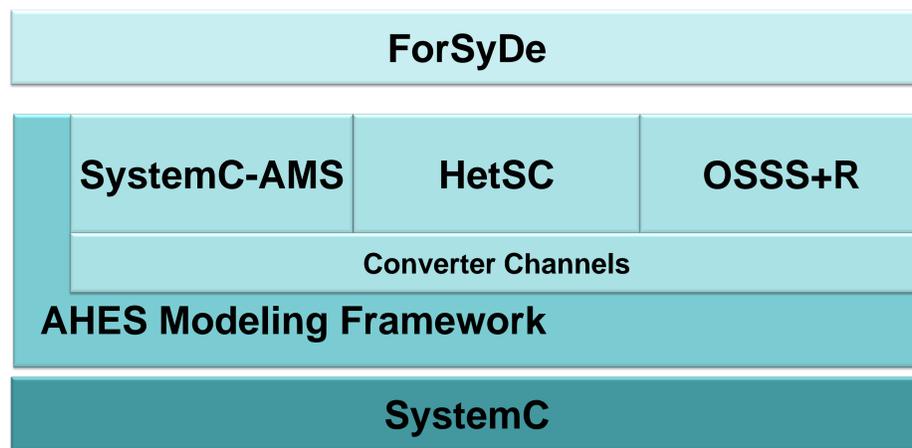
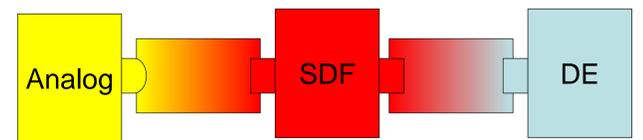
## Formal performance analysis



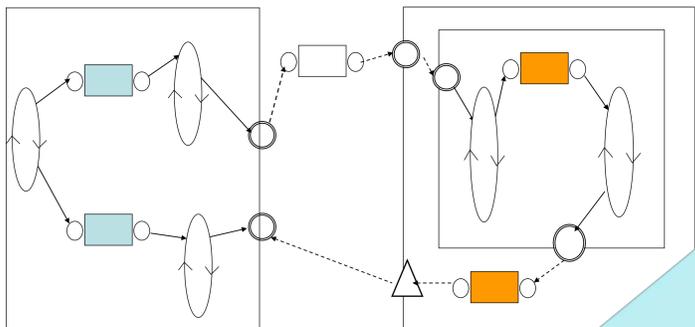
## Analog/Mixed-Signal Modeling



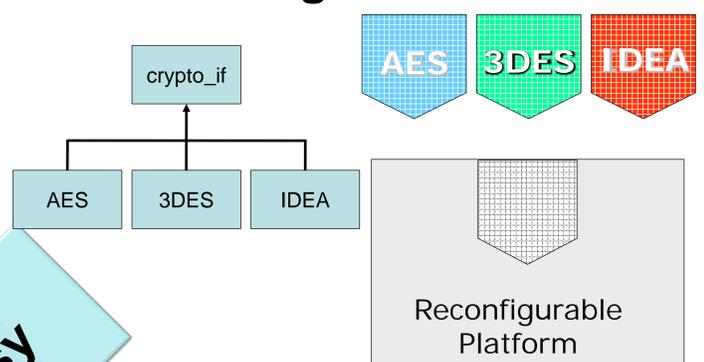
## Self-adapting converter channels



## Embedded SW Modeling



## Object-oriented modeling of run-time reconfigurable hardware



SWGen

Fossy



Automatic HW/SW Synthesis