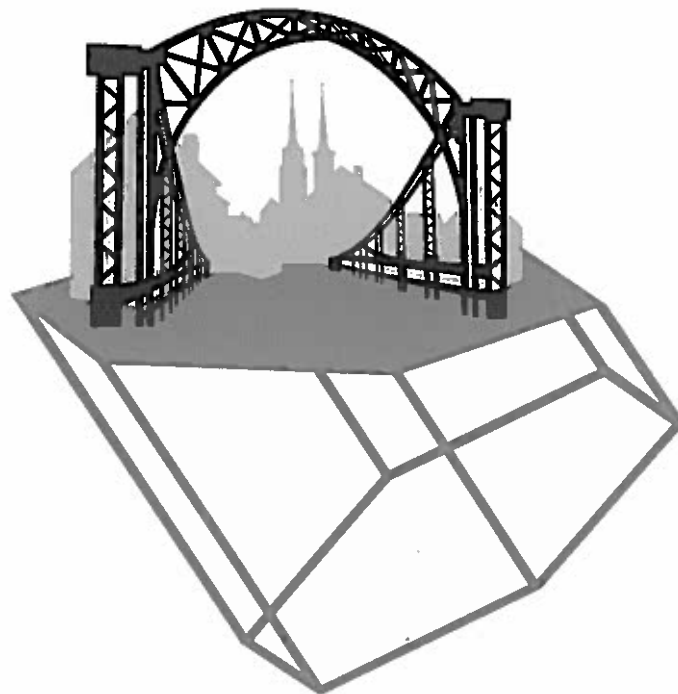


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TuBP6

Effect of III-N Barrier Resistance on CV Characteristics in GaN-based MOSHEMTs in Spill-Over Regime.

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TuBP7

Type-II In0.17Al0.83N/GaN Band alignment: Experimental Evidence and Theoretical Interpretation.

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TuBP8

Carrier dynamics in blue- and green-emitting InGaN/GaN quantum wells.

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TuBP9

A Study of the Inclusion of Prelayers in InGaN/GaN Single and Multiple Quantum Well Structures.

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TuBP10

Correlation of Microstructure and Optical Properties of InGaN Quantum Wells on Metamorphic Buffer.

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TuBP11

InGaN Quantum Wells on Semipolar Planes: Comparison of Growth and Optical Emission Properties on (20-21) and (20-2-1).

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TuBP12

Optical Studies of Non-Polar m-Plane (1-100) InGaN/GaN MQWs Grown on Freestanding Bulk GaN.

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TuBP13

Importance of the dielectric contrast for the polarization of the photoluminescence of GaN nanowires.

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TuBP14

Luminescence Properties of Semipolar (11-22) InGaN Single Quantum Wells.

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Effect of III-N Barrier Resistance on CV Characteristics in GaN-based MOSHEMTs in Spill-Over Regime

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Due to low gate leakage current and high gate voltage swing, GaN-based Metal Oxide Semiconductor High Electron Mobility Transistors (MOSHEMTs) are attractive for power electronic applications. However, they still suffer from threshold voltage (V_{th}) instability under forward bias conditions [1-4]. In the spill-over regime, where a second conductive channel is formed at the III-N/dielectric interface, the onset of the second capacitance rise is usually strongly frequency dependent [1]. It has recently been shown that the threshold voltage dynamics is governed not only by the capture and emission processes of traps at/near the dielectric/III-N interface but also by the effect of the conductance of the III-N barrier [2, 3]. Yang et al. also pointed out that the III-N barrier resistance can influence the extracted trap density using the conductance technique [4]. Therefore, they proposed to analyze the frequency and temperature dependence of the onset of the second rise in the CV curve for the trap density evaluation. In this paper, using a lumped element model with bias-dependent elements, we show that the frequency shift in the second capacitance rise can be solely explained by the effect of the barrier resistance, without considering the response of traps.

The heterostructure composed of 2 nm GaN cap layer, 25 nm $Al_{0.25}Ga_{0.75}N$, GaN channel and AlGaN nucleation layer was grown by MOCVD on highly doped silicon substrate. A 30nm SiO_2 layer was chemical vapor deposited on top of the heterostructure. Capacitor top electrode is composed by Pt/Au (area of $2000 \mu m^2$). The CV analysis was performed with a Keithley 4100 using an equivalent parallel capacitance/conductance model. The voltage sweep was performed several times for each frequency, so that some typical quasi-stable hysteresis was observed in a group of curves. The group of curves is thus considered as typical and reproducible for this frequency.

The measured CV curve shows a typical shift in the second onset voltage, V_{so} , and a decrease of the second capacitance plateau with increasing frequency (Fig. 1). We simulate the CV curve (in general admittance) using a lumped element model in Fig. 2, considering that the capacitance increase at V_{so} is caused by the strong decrease in the barrier resistance R_{br} with increasing bias. This can be explained physically by the decrease of the barrier potential with increasing gate bias. This will gradually short-circuit the barrier capacitance leading to a capacitance increase, up to C_{ox} . The full model (Fig. 2), based on [5] includes: (i) the gradual decrease of the channel differential resistance R_c around V_{th} , which is related to inverse of transconductance of a HEMT device fabricated on the same structure, (ii) bias dependence of depletion capacitance C_d extracted from Fig.1 according to [5], (iii) gradual decrease of R_{br} from the region of $V < V_{so}$ to the region of $V > V_{so}$ where R_{br} saturates at a value consistent with the differential resistance of the bias-rescaled gate leakage current of a Schottky diode prepared on the same heterostructure. We point out again that in this model the response of traps is not included. The calculated capacitance and conductance curves according to this model (Fig.3) can explain all the main features of the experiments considering only the frequency dependent response of the equivalent circuit of Fig. 2. The shift in V_{so} is determined by the width of the transition region in R_{br} (see Fig.3c). The change in the capacitance plateau is due to finite value of R_{br} for $V > V_{so}$. In addition, the model