

[Login](#)[Lost password ? Create account](#)**Main menu**

- [Welcome](#)
- [Scope of Papers](#)
- [Program ▼](#)
- [Schedule at a glance](#)
- [Full Program](#)
- [Tutorials](#)
- [Keynote Speeches](#)
- [Invited Talks](#)
- [Invited awarded papers](#)
- [Workshops](#)
- [Registration ▼](#)
- [Author's Corner ▼](#)
- [Exhibiton ▼](#)
- [Sponsoring](#)
- [Committees ▼](#)
- [Accommodation](#)
- [Conference Venue](#)
- [Visit Toulouse](#)

HELP

- [@ Contact](#)

Program > Full Program**ESREF2019 CONFERENCE PROGRAM****Monday, September 23**

		Tutorial 1
		Room Guillaumet 1
08:50	<i>The Future of Reliability Testing</i> J. Mcpherson McPherson Reliability Consulting LLC	
		Tutorial 3
		Room Guillaumet 2
08:50	<i>Introducing layered dielectrics in solid-state microelectronic devices</i> M. Lanza Institute of Functional Nano & Soft Materials, Soochow University	
10:30	Coffee Break	
		Tutorial 2
		Room Guillaumet 1
10:50	<i>Reliability of Power Electronic Packaging</i> O. Wittler, A. Middendorf Fraunhofer IZM	
		Tutorial 4
		Room Guillaumet 2
10:50	<i>Methodology of soft error expertise applied for the use of embedded electronic devices in natural radiation environments</i> L. Artola ONERA - The French Aerospace Lab	
12:30	Lunch	

Session C1 Progress in Failure Analysis: Defect Detection and Analysis

Room Cassiopée

chairpersons I. DE WOLF
J. GOXE

08:30 Invited paper
A completely new scanning electron microscope (SEM)
R. Schröder
Univ. Heidelberg

09:10 C1-1 #193 *Cold temperature power on reset use case*
M. Van Veenhuizen, D. Looijmans, M. Vogels, M. Van Aalten, P. Van Der Cruijsen, P. Gibas, S. Ersoy
NXP Semiconductors

09:30 C1-2 #109 *Convolutional neural network (CNNs) based image diagnosis for failure analysis of power devices*
A. Watanabe, N. Hirose, H. Kim, I. Omura
Kyushu Institute of Technology

09:50 C1-3 #250 *Using infrared thermal responses for PCBA production tests: Feasibility study*
N. El Belghiti Alaoui¹, A. Cassou², P. Tounsi², A. Boyer²,
A. Viard¹
¹ACTIA Automotive, ²LAAS

10:10 C1-4 #163 *Quantitative fractography analysis of a chip crack in a Si power MOSFET*
R. Schneider
Infineon Technologies AG

Session F2-1 GaN devices reliability

Room Guillaumet 1

chairpersons F. MORANCHO
L. THEOLIER

09:10 F2-1-1 #185 *Buffer breakdown in GaN-on-Si HEMTs: a comprehensive study based on a sequential growth experiment*
M. Borga¹, M. Meneghini¹, D. Benazzi¹, E. Canato¹,
R. Püsche², J. Derluyn², F. Medjdoub³, I. Abid³,
G. Meneghesso¹, E. Zanoni¹
¹Department of information engineering, University of Padova, ²EpiGaN,
³CNRS

09:30 F2-1-2 #167 *Stability and degradation of isolation and surface in Ga₂O₃ devices*
C. De Santi¹, A. Nardo¹, M. H. Wong², K. Goto³,
A. Kuramata⁴, S. Yamakoshi⁵, H. Murakami³,
Y. Kumagai³, M. Higashiwaki², G. Meneghesso¹,
E. Zanoni¹, M. Meneghini¹
¹University of Padova, ²National Institute of Information and Communications Technology, ³Department of Applied Chemistry, Tokyo University of Agriculture and Technology, ⁴Novel Crystal Technology, Inc.,
⁵Tamura Corporation

09:50 F2-1-3 #177 *Characterization of charge trapping mechanisms in GaN vertical Fin FETs under positive gate bias*
M. Ruzzarin¹, C. De Santi¹, F. Chiocchetta¹, M. Sun²,
T. Palacios², E. Zanoni¹, G. Meneghesso¹,
M. Meneghini¹
¹University of Padova, ²MIT

10:10 F2-1-4 #111 *Stress and Recovery Dynamics of Drain Current in GaN HD-GITs Submitted to DC Semi-ON Stress*
V. Padovan¹, C. Koller², G. Pobegen², C. Ostermaier³,
D. Pogany⁴